



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Park et al.
Assignee: Samsung Electronics Co. Ltd.
Title: Semiconductor Memory Device And Method Of Manufacturing For Preventing Bit Line Oxidation
Serial No.: 09/008,525 Filing Date: June 16, 1998
Examiner: Nguyen, Cuong Quang Group Art Unit: 2811
Docket No.: AB-778 US

#14/election
RECEIVED
OCT 28 2002
TECHNOLOGY CENTER
2811

San Jose, California
October 23, 2002

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

RESPONSE TO RESTRICTION

Dear Sir:

In response to the Restriction Requirement in the above-identified application having a mailing date of October 9, 2002, Applicants hereby elect Claims 1-35 with traverse.

EXPRESS MAIL LABEL NO:

EV 160 614 905 US

Respectfully submitted,

Norman R. Klivans
Attorney for Applicants
Reg. No. 33,003

LAW OFFICES OF
SKJERVEN MORRILL LLP

25 METRO DRIVE
SUITE 700
SAN JOSE, CA 95110
(408) 453-9200
FAX (408) 453-7979